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CONFIRMATION NO. 6609

SERIAL NUMBER 10/518,779	FILING OR 371(c) DATE 06/06/2005 RULE	CLASS 034	GROUP ART UNIT 1700	ATTORNEY DOCKET NO. STFD.035US
APPLICANTS Kailash Gopalakrishnan, Mountain View, CA; James D. Plummer, Portola Valley, CA;				
** CONTINUING DATA ***** This application is a 371 of PCT/US03/19279 06/19/2003				
** FOREIGN APPLICATIONS ***** UNITED STATES OF AMERICA 60390821 06/19/2002				
** SMALL ENTITY **				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		STATE OR COUNTRY CA	SHEETS DRAWING 15	TOTAL CLAIMS 43
Verified and Acknowledged Examiner's Signature _____ Initials _____		INDEPENDENT CLAIMS 12		
ADDRESS 40581				
TITLE Insulated-gate semiconductor device and approach involving junction-induced intermediate region				
FILING FEE RECEIVED 2040	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	